



BAT54DW

DUAL SURFACE MOUNT SCHOTTKY BARRIER DIODE

Features

Low Forward Voltage Drop

Fast Switching

Ultra-Small Surface Mount Package PN Junction Guard Ring for Transient and ESD Protection

Lead Free/RoHS Compliant (Note 3)

Mechanical Data

Case: SOT-363

Case Material: Molded Plastic. UL Flammability

Classification Rating 94V-0

Moisture Sensitivity: Level 1 per J-STD-020C Terminals: Solderable per MIL-STD-202, Method 208

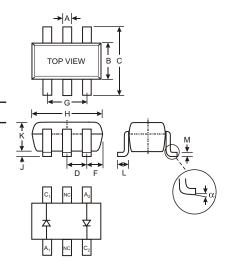
Lead Free Plating (Matte Tin Finish annealed over Alloy 42

leadframe).

Terminal Connections: See Diagram

Marking: KLD (See Page 2)

Weight: 0.006 grams (approximate)



	SOT-363								
Dim	Min	Max							
Α	0.10	0.30							
В	1.15	1.35							
С	2.00 2.20								
D	0.65 N	ominal							
F	0.30 0.40								
Н	1.80	2.20							
J		0.10							
K	0.90	1.00							
L	0.25	0.40							
M	0.10 0.2								
	0	8°							
All Din	nensions	in mm							

Maximum Ratings @ TA = 25 C unless otherwise specified

Characteristic		Symbol	Value	Unit		
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V _{RRM} V _R WM V _R	30	V		
Average Rectified Forward Current		I _O	100	mA		
Forward Continuous Current (Note 1)		I _F	200	mA		
Repetitive Peak Forward Current (Note 1)		I _{FRM}	300	mA		
Forward Surge Current (Note 1)	@ t < 1.0s	I _{FSM}	600	mA		
Power Dissipation (Note 1)		P _d	200	mW		
Thermal Resistance, Junction to Ambient Ai	r (Note 1)	R _{JA}	625	C/W		
Operating and Storage Temperature Range		T _j , T _{STG}	-65 to +125	С		

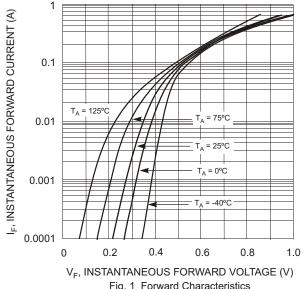
Electrical Characteristics @ TA = 25 C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 2)	V _{(BR)R}	30			V	I _{RS} = 100 A
Forward Voltage	V _F			240 320 400 500 1000	mV	I _F = 0.1mA I _F = 1mA I _F = 10mA I _F = 30mA I _F = 100mA
Reverse Leakage Current (Note 2)	IR			2.0	Α	V _R = 25V
Total Capacitance	CT			10	pF	V _R = 1.0V, f = 1.0MHz
Reverse Recovery Time	t _{rr}			5.0	ns	I_F = 10mA through I_R = 10mA to I_R = 1.0mA, R_L = 100

Notes: 1. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

- 2. Short duration test pulse used to minimize self-heating effect.
- 3. No purposefully added lead.





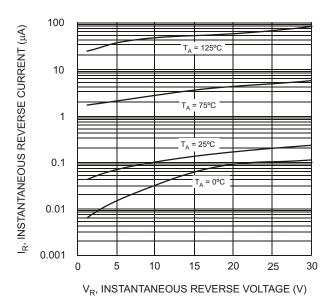


Fig. 1 Forward Characteristics

12 f = 1.0MHz 10 8 6 0 0 5 10 15 20 25 30

0 0 25

P_{D,} POWER DISSIPATION (mW) 200 100

Fig. 2 Typical Reverse Characteristics

V_R, REVERSE VOLTAGE (V) Fig. 3 Typical Capacitance vs. Reverse Voltage

T_A, AMBIENT TEMPERATURE (°C) Fig. 4 Power Derating Curve

75

100

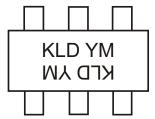
125

Ordering Information (Note 4)

Device	Packaging	Shipping		
BAT54DW-7-F	SOT-363	3000/Tape & Reel		

4. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



KLD = Product Type Marking Code YM = Date Code Marking Y = Year ex: N = 2002M = Month ex: 9 = September

Date Code Key

C_T, TOTAL CAPACITANCE (pF)

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	М	N	Р	R	S	Т	U	V	W	Х	Υ	Z

Month	Jan	Feb	March	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



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